

ABSTRACT OF THE DISCLOSURE

A signal processing circuit has a sample/hold circuit for sampling an input signal comprised of a first signal and a second signal and for holding the first signal. The first signal comprises an optical signal obtained due to storage of electric charges generated due to light incident upon a photoelectric converter, and the second signal comprises a reference signal obtained due to resetting of the photoelectric converter. A subtracter receives an output signal of the sample/hold circuit and the input signal and obtains a difference between the output signal of the sample/hold circuit and the input signal. A voltage clamp circuit clamps a part or all of an output signal from the subtracter.